

ABSTRACT

An apparatus is provided for effectively supporting a microprocessor development system by using a single chip without designing an evaluation chip. The apparatus includes a microprocessor control unit (MCU) chip for communicating data and control signals with respect to a target board through a multiplicity of ports, and for providing RAM addresses, a special function register (SFR) address and data. The apparatus also includes an input/output block for receiving the addresses and the data through the ports, for providing the received data to the target board, and for controlling data to be inputted in the MCU chip.

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